

PLECS WEBSESSION

Advanced Modeling and Simulation of Power Electronic Systems

Warsaw University of Technology and PEL/IES Joint Chapter, December 3, 2020

12:00	Magnetic Modeling & Simulation <ul style="list-style-type: none">▶ Reluctance-resistance analogy approach▶ Permeance-capacitance analogy approach▶ Implementation in PLECS▶ Magnetic component library
12:45	<i>Optional - Break</i>
13:00	Introduction to the RT Box <p>Introduction</p> <ul style="list-style-type: none">▶ Important modelling principles in PLECS <p>Real-time testing concepts</p> <ul style="list-style-type: none">▶ Rapid control prototyping (RCP)▶ Hardware-in-the-loop (HIL) <p>The PLECS Coder</p> <ul style="list-style-type: none">▶ From offline to real-time▶ Target support packages <p>The hardware</p> <ul style="list-style-type: none">▶ Hardware specifications and architecture
13:45 14:00	Principle of a Processor-in-the-Loop Simulation <ul style="list-style-type: none">▶ Integration of PIL▶ Framework into an embedded project
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